

KANESAKA AND TAKEUCHI
1423 Powhatan Street
Alexandria, Virginia 22314
Tel (703) 519-9785

Case No. D-1026

Date 11/27/2000

THE COMMISSIONER OF PATENTS AND TRADEMARKS
WASHINGTON, D. C. 20231

15772 U.S. PRO
09/12/00
11/27/00

Sir:

Transmitted herewith for filing is the patent application of:

Inventor: Takayuki Takemoto

For: FLUOROSCOPY IMAGE APPARATUS

Enclosed are:

- ☒ 04 sheets of drawings (☒ formal ☐ informal).
- ☒ 18 pages of specification.
- ☒ Declaration.
- ☐ Information disclosure statement and ___ reference(s).
- ☐ Preliminary amendment.
- ☐ Verified statement of Small Entity Status.
- ☒ Assignment to Shimadzu Corporation.
- ☐ Charge \$40 to my Deposit Account No. 11-0219 for recording Assignment.
- ☐ Inventor's information sheet.

CLAIMS FILED

<u>FOR</u>	<u>Number filed</u>	<u>Number Extra</u>	<u>Rate</u>	<u>Basic Fee \$710.00</u>
Total Claims	<u>06</u> (over 20)	_____	x \$18	_____
Independent Claims	<u>01</u> (over 3)	_____	x \$80	_____
<input type="checkbox"/> Multiple dependent claim			\$270	_____
<input type="checkbox"/> Reduce by 50% for small entity				_____
<input type="checkbox"/> Foreign language filing fee			\$130	_____
TOTAL FILING FEE				<u>\$710.00</u>

- [] Please charge my Deposit Account No. 11-0219 in amount of _____
A duplicate copy of this sheet is attached.
- [X] Please charge any further filing fee, extension fee under
37 CFR 1.17(a), (b), (c) and (d) and other fee in prosecuting
the application (except issue fee) in connection with this
application to Patent Office Deposit Account No. 11-0219.
- [X] A check in the amount of \$ 750.00 to cover the filing fee and
assignment fee is enclosed.
- [X] Applicant hereby claims the benefit of the filing date of the following
foreign application under the provisions of 35 USC 119(a)-(d) of which
certified copies
- [] will follow [X] are enclosed

Japanese Patent Application
No. 11-375429 filed on December 28, 1999.

KANESAKA AND TAKEUCHI

by Manabu Kanesaka
Manabu Kanesaka
Reg. No. 31,467
Agent for Applicant

FLUOROSCOPY IMAGE APPARATUS

Background of the Invention and Related Art Statement

5 The present invention relates to an image apparatus which can see or visualize a fluoroscopy image at real time, and in particular, the present invention relates to a fluoroscopy image apparatus for the purpose of a medical diagnosis.

10 In recent years, there has been developed a radiation image-taking apparatus or radiography apparatus using a semiconductor image-taking sensor or image sensor in place of X-ray films or imaging plates which have been used in a simple radiography apparatus. There are two types of image sensors. A first type of the image sensor is formed of an X-ray converting layer which
15 converts X-ray into light; photodiode arrays which are arranged in a matrix form right under the X-ray converting layer; and TFT switches connected to the respective photodiode arrays. In the first type of the image-taking sensor, after irradiation of X-ray, the respective TFT switches are sequentially turned on, so that
20 signal charges stored in respective pixels are read out to form an X-ray image.

25 A second type of the image sensor has radiation sensor arrays formed of converting layer which responds to the radiation to directly output charge signals corresponding to incident dose or amount, and TFT switches are connected to electrodes disposed in a matrix form right under the radiation sensor arrays. In the second type of the image sensor, the respective TFT switches are sequentially turned on at the time of irradiation, so that signal

charges stored in respective pixels are read out to form an X-ray image. Hereunder, the first type of the image sensor will be explained.

Fig. 3 and Fig. 4 show a conventional radiation image apparatus of the aforementioned first type. This apparatus is formed of an image-taking sensor or image sensor 1; a gate driver circuit 2 for sequentially taking out charge signals from the respective pixels 14; a readout amplifier 3 for reading the charge signals from the respective pixels 14; and a control circuit 4 for controlling the gate driver circuit 2 and the readout amplifier 3. And, X-ray picture digital data signals from the image sensor 1 are outputted from the readout amplifier 3 to an outside.

In the image sensor 1, the pixels 14 are regularly arranged two-dimensionally, i.e. in the length and breadth directions. The gate driver circuit 2 is driven by a signal from the control circuit 4, so that pulse signals are sequentially sent to the respective pixels 14 from the line direction through gate lines 13. On the other hand, the readout amplifier 3 is driven by a signal from the control circuit 4, so that X-ray picture charge signals in the respective pixels 14 are sequentially read out from the row or columnar direction through readout signal lines 12.

In each pixel 14, there is a photoelectric conversion element. Namely, a scintillator emits light in response to an incidence intensity of X-ray, and the emitted light enters into a photodiode to generate a charge signal. At a position located at a side opposite to an X-ray incidence side and corresponding to each pixel 14, a transistor switch, for example, an FET having a signal readout switching function, is arranged two-dimensionally in each

pixel 14. By switch pulses from the gate driver circuit 2 through the gate lines 13, the charge signals are read out to the readout amplifier 3 through the readout signal lines 12.

5 The X-ray picture digital data signals from the image sensor 1 are outputted from the readout amplifier 3 to the outside. However, since the sensitivities of the respective pixels 14 of the image sensor 1 are irregular and there might be defective pixels, a correction processing circuit 5 is generally provided to carry out a correction process, such as a gain correction, and at the same time, the correction processing circuit 5 carries out an offset correction (correction for returning to a zero level when there is no X-ray input). Furthermore, in order to see the X-ray image better, an image processing circuit 6 is provided to carry out an image processing, such as edge enhancement for the X-ray picture digital data signals, or smoothing.

10 As described above, one X-ray photograph is taken by spending a time, or images are taken at real time in the rate of more than ten images per second.

15 The conventional fluoroscopy image apparatus is structured as described above, and in the ordinary sequence control, a control section for controlling an entire system is often used as a system which combines a movement control mechanism for an X-ray generator, an X-ray tube, an examination table, a flat panel type image-taking apparatus or the like, and a data collection control mechanism.

20 And, as the system becomes complicated, a possibility of a system failure of the control section for controlling the system to be inoperative becomes higher. For example, in case an operation is performed by using the aforementioned apparatus, the system failure

of the control circuit for controlling the system resulting in failure to obtain the X-ray image means losing an important tool for the operation. In this case, there is a task that if only the X-ray generation in the apparatus is possible, a control system of the conventional flat panel type image-taking apparatus might be switched to a mechanism which can individually control, so that the output signal can be taken out to be observed by a general TV monitor.

The present invention has been made in view of the foregoing, and an object of the invention is to provide a fluoroscopy image apparatus, wherein even if a control system is shut down and an X-ray image can not be obtained while operating the apparatus, in case the X-ray generation in the apparatus is possible, the flat panel type image-taking apparatus can be individually operated and an output signal can be taken out, so that the X-ray image can be observed by a general TV monitor.

Further objects and advantages of the invention will be apparent from the following description of the invention.

Summary of the Invention

To achieve the aforementioned object, the present invention provides a fluoroscopy image apparatus, comprising: two-dimensional radiation sensor arrays formed of photoelectric conversion elements for outputting charge signals corresponding to an incident amount by responding to a radiation; TFT switches arranged in a matrix form right under the radiation sensor arrays and connected to the photoelectric conversion elements; a gate driver circuit for turning on the respective TFT switches in case of reading out the

signals; a readout amplifying circuit for reading out charge signals stored in respective pixels; a control circuit for controlling the gate driver circuit and the readout amplifying circuit; a TV reference signal circuit formed of a horizontal scanning/synchronization pulse waveform generating circuit and a vertical scanning/synchronization pulse waveform generating circuit; and a picture signal superimposed circuit for driving the gate driver circuit by a signal from the TV reference signal circuit and taking out picture signals from the radiation sensor arrays through the readout amplifying circuit. The picture signal superimposed circuit superimposes the picture signals on the signal from the TV reference signal circuit by synchronizing with the signal from the TV reference signal circuit, and outputs a TV analog video signal.

Also, in the apparatus of the invention, the control circuit controls the gate driver circuit and the readout amplifying circuit so that a plurality of pixels is joined as one pixel unit when the charge signals of the radiation sensor arrays are read out and scanned.

Further, the apparatus of the invention is provided with wireless transmitting means which can wirelessly transmit the analog video output signal.

Also, the apparatus of the invention is provided with a digital-to-analog switching circuit which is capable of switching between a digital video control and an analog video control to use one of the control.

The fluoroscopy image apparatus of the invention is structured as described above. The gate driver circuit is driven by signals

from the TV reference signal circuit, and the picture signals from the radiation sensor arrays are taken out through the readout amplifying circuit. Also, the picture signals are superimposed by synchronizing the signals from the TV reference signal circuit, and the TV analog video signal. Therefore, apart from the ordinary control sequence, in the individual flat panel type image-taking apparatus, the X-ray image can be displayed in the monitor.

Also, since the gate driver circuit and the readout amplifier are controlled to be driven so that a plurality of pixels is combined as one pixel unit when the charge signals of the radiation sensor arrays are read out and scanned, the effective sensor region can be further enlarged.

Further, since the analog video output signals can be transmitted to the monitor by wireless transmitting means which is capable of transmitting wirelessly, a cable is not necessary, and the X-ray image can be observed by the monitor at a remote place.

Also, since the apparatus of the invention is provide with the digital-to-analog switching circuit which switches between the digital video control and the analog video control to use the switched control, the monitor disposed beside the apparatus and the monitor at the remote place can be switched to each other to be used in accordance with the necessity.

Brief Description of the Drawings

Fig. 1 is a diagram showing an embodiment of a fluoroscopy image apparatus of the invention;

Figs. 2(a) through 2(d) are explanatory views for explaining a TV scanning system of the fluoroscopy image apparatus of the

invention;

Fig. 3 is a diagram showing a conventional fluoroscopy image apparatus; and

Fig. 4 is an explanatory view for explaining a scanning of an image sensor of the conventional fluoroscopy image apparatus.

Detailed Description of Preferred Embodiments

Embodiments of a fluoroscopy image apparatus of the invention will be explained with reference to Fig. 1. Fig. 1 is a view showing a picture signal system diagram of the fluoroscopy image apparatus of the invention.

The apparatus of the invention is formed of an image-taking sensor or image sensor 1 for generating charges of an X-ray picture in response to incident X-ray intensity; a gate driver circuit 2 for sequentially taking out charge signals from respective pixels 14; a readout amplifier 3 for reading the charge signals from the respective pixels 14; a control circuit 4, which includes a pixel control circuit 20 and D/A (digital-to-analog) switching circuit 21 and controls the gate driver circuit 2 and the readout amplifier 3; a TV reference signal circuit 7 which includes a horizontal scanning/synchronization pulse waveform generating circuit 7a and a vertical scanning/synchronization pulse waveform generating circuit 7b; a picture signal superimpose circuit 8, which drives the gate driver circuit 2 by a signal from the TV reference signal circuit 7 and takes out the picture signals from the image sensor 1 through the readout amplifier 3, so that the picture signals are superimposed by synchronizing to the signal from the TV reference signal circuit 7 to output a TV analog video signal; wireless

transmitting means 9 for wirelessly transmitting the analog video signal to the outside without using a cable; and a correction processing circuit 5 which performs a sensitivity correction and an offset correction.

5 As in the apparatus shown in Fig. 4, in the image sensor 1, the pixels 14 are regularly arranged two-dimensionally. Also, the gate driver circuit 2 is driven by the signal from the control circuit 4 so that pulse signals are sequentially sent to the respective pixels 14 from the line direction through gate lines 13.

10 On the other hand, the readout amplifier 3 is driven by signals from the control circuit 4, and X-ray picture signals in the respective pixels 14 are read out from the column or row direction through readout signal lines 12.

15 In the control circuit 4, there are provided the pixel control circuit 20 and the D/A switching circuit 21 which are added to the conventional circuit. And, the control circuit 4 sends the signals to the gate driver circuits 2 and the readout amplifier 3 to drive the same. When an ordinary digital video signal is outputted, the respective pixels 14 are sequentially scanned as in the

20 conventional apparatus. However, as the fluoroscopy image apparatus of the invention, in case an analog video signal is to be outputted, the D/A switching circuit 21 provided in the control circuit 4 is switched to the analog side, and the pixel control circuit 20 is switched when the charge signals of the image sensor

25 1 are read out and scanned, so that the gate driver circuit 2 and the readout amplifier 3 are controlled and driven by making a plurality of the pixels 14 as one pixel unit. Accordingly, an effective sensor region can be enlarged.

In the TV reference signal circuit 7, in order to read out and scan the charge signals of the image sensor 1 by a standard TV scanning system, the horizontal scanning/synchronization pulse waveform generating circuit 7a and the vertical scanning/synchronization pulse waveform generating circuit 7b are provided. By the signal from the TV reference signal circuit 7, the gate driver circuit 2 is controlled, and by the signal from the control circuit 4, the charge signals of the image sensor 1 are read out and scanned by the standard TV scanning system. Also, a synchronizing signal of the horizontal scanning/synchronization pulse waveform generating circuit 7a and a synchronizing signal of the vertical scanning/synchronization pulse waveform generating circuit 7b are simultaneously sent to the picture signal superimpose circuit 8.

In the picture signal superimpose circuit 8, the picture signals, outputted from the X-ray charge signals of the image sensor 1 through the readout amplifier 3 based on the TV standard scanning system, are synchronized with the horizontal scanning synchronization pulse and the vertical scanning synchronization pulse from the TV reference signal circuit 7, and superimposed on the TV standard scanning signal to output as the standard analog TV scanning signals to the outside. By this standard analog TV scanning signals, the X-ray image can be displayed in an external general TV monitor.

The wireless transmitting means 9 is provided with an antenna or an infrared ray source, and in order to send the analog video signal to the external monitor without using the cable, the analog video signal can be sent to the external monitor by using the

wireless transmitting means 9, such as a radio wave or an infrared ray.

The correction processing circuit 5 performs a correction process, such as a gain correction, since the sensitivities of the
5 respective pixels 14 are not uniform and there is a case that defective pixels exist. At the same time, the correction processing circuit 5 performs an offset correction for correcting a readout signal to a zero level when there is no X-ray input.

10 Figs. 2(a) through 2(d) show views for explaining a standard TV scanning method of the apparatus of the invention. The method is explained along NTSC (National Television System Committee) as the standard scanning. In the NTSC, there is used an interlace system with 525 scanning lines and aspect rate of 4 to 3. The
15 image sensor 1 normally includes a matrix of several thousands \times several thousands, for example, the pixels 14 of $2,000 \times 2,000$, and signals are read out per row by switching the respective lines to obtain the X-ray image. Therefore, a plurality of pixels 14 is
20 joined and scanned as one pixel. For example, three pixels in a horizontal direction are bracketed or joined as one pixel unit and three pixels in a vertical direction are bracketed or joined as one
25 pixel unit, so that in 525 scanning lines, a number of pixels both in the vertical direction and the horizontal direction is $525 \times 3 = 1,575$, and these 1,575 pixels are effectively used. When the pixel control circuit 20 provided in the control circuit 4 is switched and set, the number of the pixels can be changed.

First, as shown in Fig. 2(a), the image sensor 1 is sequentially scanned as 1, 3, 5, 7, ... 525 from an upper left in an upper section, and then scanned as 2, 4, 6, 8, ... 524 from the

upper left in the upper section. By the interlace with 2 to 1, one frame is structured. Fig. 2(b) shows a horizontal scanning waveform, and a single serrate wave constitutes a scanning drive voltage waveform. Fig. 2(c) shows a vertical scanning waveform, and the surface to be scanned is interlaced and scanned twice. Fig. 2(d) shows a picture signal 15 superimposed on the TV standard scanning signal. An abscissa constitutes a time axis, and Fig. 2(d) shows one horizontal scanning signal. A white level 16 is set at a lower portion of the figure, and designates a level in the condition that there is no input signal. In order to synchronize an interval from a start point of the scanning line to a turning point, a horizontal synchronization pulse 17 is set, and while the line is being returned in scanning, a blanking pulse 19 is set to be a black level. The vertical synchronization pulse is set similarly, and when a signal is being returned vertically, a blanking pulse is set to be a black level 18.

In the apparatus of the invention, the control circuit 4 used at the time of generating the digital video signal is not used at the time of generating the analog video signal, and switched by the D/A switching circuit 21, so that the TV reference signal circuit 7 drives the gate driver circuit to scan. Namely, the TV reference signal circuit 7 has a mode which outputs analog image signals (TV signals of NTSC (EIA) and PAL (CCIR)).

There are 525 scanning lines in NTSC, and there are 625 scanning lines in PAL. These scanning lines are allowed to respond to the respective lines of the image sensor 1, and the signals are sequentially outputted in the row direction, so that these analog signals can be outputted.

Incidentally, although NTSC (EIA) and PAL (CCIR) are the TV signals having an aspect ratio of 4 to 3, TV signals are not limited to these signals, and can be TV signals with other aspect ratio.

5 Namely, in the image sensor 1, apart from the sequence mode which switches the respective lines to collect the images based on the signal from the control circuit 4, for example, in case of the NTSC (EIA), there may be provided the TV reference signal circuit 7 for forming the reference signal, such as TV frame synchronization signal regulated to have a horizontal scanning frequency of 15.75 Hz and a vertical scanning frequency of 60 Hz, and the picture signal superimpose circuit 8 which switches the respective lines in response to these signals and mixes these signals with the synchronization signals to output the mixed signals. Also, an analog output terminal is provided.

10
11
12
13
14
15
16
17
18
19
20 Then, the correction processing circuit 5 is disposed in the vicinity of the readout amplifier 3, so that the sensitivity correction and the offset correction are carried out per pixel 14 at real time by using a correction table. Therefore, even though the image processing, such as an edge enhancement and smoothing, can not be operated, only the sensitivity correction and the offset correction can be performed inside a flat panel main body. In this case, preferably, the signal of each pixel 14 is once converted from analog to digital to be a digital signal, and after correction operation, the signal is converted from digital to analog to be an analog signal.

25 Further, addition of analog signals switched every two pixels 14 can be operated both on the analog using a multiplexer and on

the digital. For example, it is presumed that the number of the pixels 14 of the image sensor 1 is 2,000 (lengthwise) \times 2,000 (breadthwise). Assuming that there are 525 vertical scanning lines and an aspect ratio of 4 to 3, at the time of scanning the image sensor, if the sensor is scanned per pixel 14, there is used a region of 525 (lengthwise) \times 700 (breadthwise). And, by switching the pixel control circuit 20 provided in the control circuit 4, vertical two pixels 14 and horizontal two pixels 14 are bracketed or joined as one pixel unit. Then, the scanning signals in the vertical direction from the pixel control circuit 20 are inputted into the gate driver circuit 2, and the readout signals in the horizontal direction are inputted into the readout amplifier 3, so that two gate lines 13 and two readout signal lines 12 are allowed to scan at the same time.

Added signals are inputted into the picture signal superimpose circuit 8 as the picture signals from the readout amplifier 3. In this case, there is used a region of 1,050 (lengthwise) \times 1,400 (breadthwise). Although the addition is operated by the digital process in the scanning method of the pixels 14 of the image sensor 1 in the foregoing, the addition can be operated by another method. For example, the pixels 14 are scanned every vertical two pixels 14 and the horizontal pixels 14 are read out per pixel 14, and front and rear signals are added by the analog signals in the readout amplifier 3.

Also, although the cable is necessary in order to send the analog video signals to the monitor, the signals can be transmitted to the monitor by using the wireless means, such as a radio wave or an infrared ray. As these wireless transmitting means and wireless

receiving means necessary in the monitor side, an ordinary known technology can be used.

The fluoroscopy image apparatus of the invention is structured as described above, and apart from the ordinary control circuit, the TV reference signal circuit for forming the reference signal, such as the TV frame synchronization signal, and the picture signal superimposed circuit, which switches the respective lines of the image sensor in response to these signals and mixes the signals with the synchronization signals to output the mixed signals, are built in the flat panel. Therefore, even if the system failure of the control circuit occurs, the X-ray image can be displayed in the monitor by the analog video output in the general TV standard scanning system.

Also, since the gate driver circuit and the readout amplifier are driven and controlled so that a plurality of pixels in the image sensor is combined as one pixel unit, the effective sensor region can be changed and selected in accordance with the size of an examined portion.

Further, since the wireless transmitting means by the radio wave or the infrared ray is provided, a cable is not necessary, and the X-ray image can be observed by the monitor at a remote place.

Also, since there is provided the digital-to-analog switching circuit which switches between the digital output and the analog output to be able to use both the outputs, the monitor in the apparatus and the monitor at the remote place can be switched to each other to be used in accordance with the necessity.

While the invention has been explained with reference to the specific embodiments of the invention, the explanation is

illustrative and the invention is limited only by the appended claims.

63
62
61
60
59
58
57
56
55
54
53
52
51
50
49
48
47
46
45
44
43
42
41
40
39
38
37
36
35
34
33
32
31
30
29
28
27
26
25
24
23
22
21
20
19
18
17
16
15
14
13
12
11
10
9
8
7
6
5
4
3
2
1

What is claimed is:

1. A fluoroscopy image apparatus, comprising:

two-dimensional radiation sensor arrays formed of
photoelectric conversion elements for outputting charge signals
5 corresponding to an incident amount of radiation,

switches arranged in a matrix form under the radiation sensor
arrays and connected to the photoelectric conversion elements,

a gate driver circuit connected to the switches for turning on
the respective switches in case of reading out the signals,

10 a readout amplifying circuit connected to the sensor arrays
for reading out the charge signals stored in respective pixels,

a control circuit connected to the gate driver circuit and the
readout amplifying circuit for controlling the same,

15 a TV reference signal circuit connected to the control circuit
and having a horizontal scanning/synchronization pulse waveform
generating circuit and a vertical scanning/synchronization pulse
waveform generating circuit, and

a picture signal superimpose circuit connected to the TV
reference signal circuit and the gate driver circuit for driving
20 the gate driver circuit by a signal from the TV reference signal
circuit and taking out picture signals from the radiation sensor
arrays through the readout amplifying circuit, said picture signal
superimpose circuit superimposing the picture signals on the
signals from the TV reference signal circuit by synchronizing with
25 the signals from the TV reference signal circuit to thereby output
a TV analog video signal.

2. A fluoroscopy image apparatus according to claim 1, further

comprising control means connected to the gate driver circuit and the readout amplifying circuit for controlling the same so that a plurality of pixels is joined as one pixel unit when the charge signals of the radiation sensor arrays are read out and scanned.

5

3. A fluoroscopy image apparatus according to claim 2, wherein said control means is included in the TV reference signal circuit to be operated in an analogue video control.

10 4. A fluoroscopy image apparatus according to claim 1, further comprising wireless transmitting means for wirelessly transmitting the outputted analog video signal.

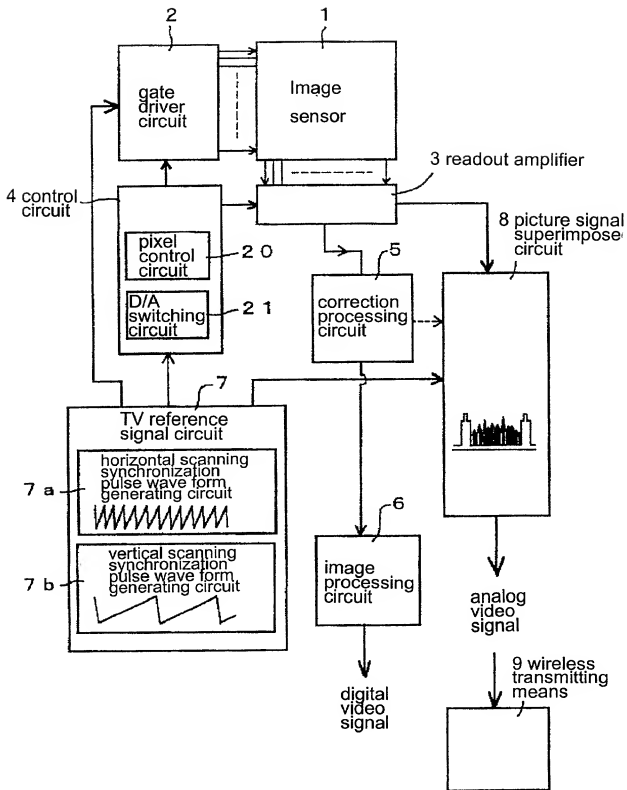
15 5. A fluoroscopy image apparatus according to claim 1, further comprising a digital-to-analog switching circuit to be able to switch between a digital video control and an analog video control so as to use one of the controls.

20 6. A fluoroscopy image apparatus according to claim 1, further comprising a correction processing circuit connected to the readout amplifier so that sensitivity correction and offset correction are carried out per sensor.

Abstract of the Disclosure

In a fluoroscopy image apparatus of the invention, in case an ordinary control circuit for an image sensor is out of order, a D/A switching mechanism is switched to analog, and a plurality of pixels of the image sensor is controlled as one pixel unit by a pixel control circuit. By TV standard scanning system signals generated in a TV reference signal circuit, the image sensor is scanned through the gate driver circuit. In a picture signal superimpose circuit, X-ray image signals from a readout amplifier are superimposed on a TV frame synchronization signal of the TV standard scanning system, and outputted as analog video signals. The analog video signals are transmitted to an external monitor to be displayed.

Fig. 1



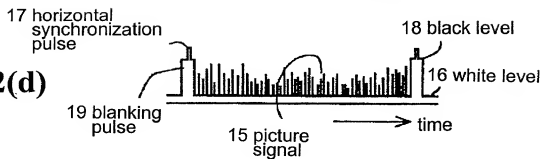


Fig. 3
Prior Art

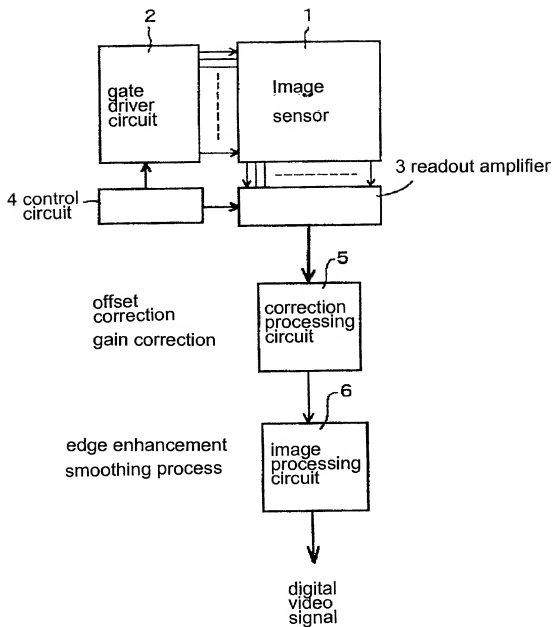
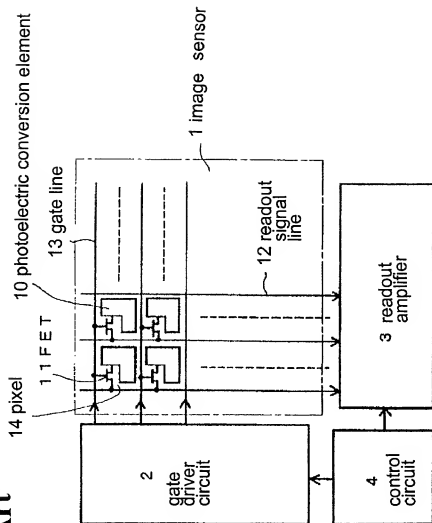


Fig. 4.



Docket No. D-1026

Declaration and Power of Attorney For Patent Application

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

FLUOROSCOPY IMAGE APPARATUS

the specification of which
(check one)

☒ is attached hereto.

☐ was filed on _____ as United States Application No. or PCT International
Application Number _____
and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d) or Section 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate or PCT International application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Priority Not Claimed

<u>11-375429</u>	<u>Japan</u>	<u>28/12/1999</u>	<input type="checkbox"/>
(Number)	(Country)	(Day/Month/Year Filed)	

_____	_____	_____	<input type="checkbox"/>
(Number)	(Country)	(Day/Month/Year Filed)	

_____	_____	_____	<input type="checkbox"/>
(Number)	(Country)	(Day/Month/Year Filed)	

I hereby claim the benefit under 35 U.S.C. Section 119(e) of any United States provisional application(s) listed below:

(Application Serial No.)

(Filing Date)

(Application Serial No.)

(Filing Date)

(Application Serial No.)

(Filing Date)

I hereby claim the benefit under 35 U.S.C. Section 120 of any United States application(s), or Section 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. Section 112. I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, C.F.R., Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

(Application Serial No.)

(Filing Date)

(Status)
(patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status)
(patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status)
(patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

Yusuke Takeuchi Reg. No. 30,921
 Manabu Kanesaka Reg. No. 31,467

Send Correspondence to:

KANESAKA AND TAKEUCHI, 1423 Powhatan Street
 Alexandria, Virginia 22314

Direct Telephone Calls to:

Manabu Kanesaka at telephone number (703) 519-9785

Full name of sole or first inventor	
Takayuki Takemoto	
Sole or first inventor's signature	Date
<i>Takayuki Takemoto</i>	<i>13/11/2000</i>
Residence	
Joyo-shi, Japan	
Citizenship	
Japanese	
Post Office Address	
1-3 Tono Nishigaito, Joyo-shi, Kyoto-fu, 610-0111, Japan	
Full name of second inventor	
Second inventor's signature	Date
Residence	
Citizenship	
Post Office Address	